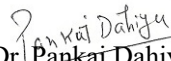


DELHI TECHNOLOGICAL UNIVERSITY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
DATESHEET FOR
END TERM PRACTICAL EXAMINATION FOR PRS AND PRE COMPONENT
NOVEMBER 2025

1st Year			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
19/11/2025	Sec A_G1 EC103_TELECOM_LAB	Sec A_G2 EC103_TELECOM_LAB	Sec A_G3 EC103_TELECOM_LAB
19/11/2025	Sec B_G1 EC103_ADSP_LAB	Sec B_G2 EC103_ADSP_LAB	Sec B_G3 EC103_ADSP_LAB
19/11/2025	Sec C_G1 EC103_MW_LAB	Sec C_G2 EC103_MW_LAB	Sec C_G3 EC103_MW_LAB
19/11/2025	Sec D_G1 EC103_VLSI_LAB	Sec D_G2 EC103_VLSI_LAB	Sec D_G3 EC103_VLSI_LAB

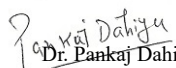
VLSI LAB	TELECOM LAB (Telecommunication Lab)	ADSP LAB (Advanced Digital Signal Processing Lab)	MW LAB (Microwave Lab)
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NOVEMBER 2025

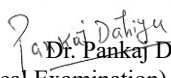
2nd Year			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
14/11/2025	Sec A_G1 EC203_DE_LAB	Sec A_G2 EC203_DE_LAB	Sec A_G3 EC203_DE_LAB
14/11/2025	Sec B_G1 EC205_VLSI_LAB	Sec B_G2 EC205_VLSI_LAB	Sec B_G3 EC205_VLSI_LAB
14/11/2025	Sec C_G1 EC207_EDC2_LAB	Sec C_G2 EC207_EDC2_LAB	Sec C_G3 EC207_EDC2_LAB
14/11/2025	Sec D_G1 EC209_TELECOM_LAB	Sec D_G2 EC209_TELECOM_LAB	Sec D_G3 EC209_TELECOM_LAB
15/11/2025	Sec A_G1 EC205_VLSI_LAB	Sec A_G2 EC205_VLSI_LAB	Sec A_G3 EC205_VLSI_LAB
15/11/2025	Sec B_G1 EC207_EDC2_LAB	Sec B_G2 EC207_EDC2_LAB	Sec B_G3 EC207_EDC2_LAB
15/11/2025	Sec C_G1 EC209_TELECOM_LAB	Sec C_G2 EC209_TELECOM_LAB	Sec C_G3 EC209_TELECOM_LAB
15/11/2025	Sec D_G1 EC203_DE_LAB	Sec D_G2 EC203_DE_LAB	Sec D_G3 EC203_DE_LAB
17/11/2025	Sec A_G1 EC207_EDC2_LAB	Sec A_G2 EC207_DE_LAB	Sec A_G3 EC207_DE_LAB
17/11/2025	Sec B_G1 EC209_TELECOM_LAB	Sec B_G2 EC209_TELECOM_LAB	Sec B_G3 EC209_TELECOM_LAB
17/11/2025	Sec C_G1 EC203_DE_LAB	Sec C_G2 EC203_DE_LAB	Sec C_G3 EC203_DE_LAB
17/11/2025	Sec D_G1 EC205_VLSI_LAB	Sec D_G2 EC205_VLSI_LAB	Sec D_G3 EC205_VLSI_LAB
18/11/2025	Sec A_G1 EC209_TELECOM_LAB	Sec A_G2 EC209_TELECOM_LAB	Sec A_G3 EC209_TELECOM_LAB
18/11/2025	Sec B_G1 EC203_DE_LAB	Sec B_G2 EC203_DE_LAB	Sec B_G3 EC203_DE_LAB
18/11/2025	Sec C_G1 EC205_VLSI_LAB	Sec C_G2 EC205_VLSI_LAB	Sec C_G3 EC205_VLSI_LAB
18/11/2025	Sec D_G1 EC207_EDC2_LAB	Sec D_G2 EC207_EDC2_LAB	Sec D_G3 EC207_EDC2_LAB


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DELHI TECHNOLOGICAL UNIVERSITY
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DATESHEET FOR
END TERM PRACTICAL EXAMINATION FOR PRS AND PRE COMPONENT
NOVEMBER 2025

3rd Year			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
14/11/2025	Sec A_G1 EC301_ADSP_LAB	Sec A_G2 EC301_ADSP_LAB	Sec A_G3 EC301_ADSP_LAB
14/11/2025	Sec B_G1 EC303_LIC_LAB	Sec B_G2 EC303_LIC_LAB	Sec B_G3 EC303_LIC_LAB
14/11/2025	Sec C_G1 EC305_MW_LAB	Sec C_G2 EC305_MW_LAB	Sec C_G3 EC305_MW_LAB
15/11/2025	Sec B_G1 EC301_ADSP_LAB	Sec B_G2 EC301_ADSP_LAB	Sec B_G3 EC301_ADSP_LAB
15/11/2025	Sec C_G1 EC303_LIC_LAB	Sec C_G2 EC303_LIC_LAB	Sec C_G3 EC303_LIC_LAB
15/11/2025	Sec D_G1 EC305_MW_LAB	Sec D_G2 EC305_MW_LAB	Sec D_G3 EC305_MW_LAB
17/11/2025	Sec C_G1 EC301_ADSP_LAB	Sec C_G2 EC301_ADSP_LAB	Sec C_G3 EC301_ADSP_LAB
17/11/2025	Sec D_G1 EC303_LIC_LAB	Sec D_G2 EC303_LIC_LAB	Sec D_G3 EC303_LIC_LAB
17/11/2025	Sec A_G1 EC305_MW_LAB	Sec A_G2 EC305_MW_LAB	Sec A_G3 EC305_MW_LAB
18/11/2025	Sec D_G1 EC301_ADSP_LAB	Sec D_G2 EC301_ADSP_LAB	Sec D_G3 EC301_ADSP_LAB
18/11/2025	Sec A_G1 EC303_LIC_LAB	Sec A_G2 EC303_LIC_LAB	Sec A_G3 EC303_LIC_LAB
18/11/2025	Sec B_G1 EC305_MW_LAB	Sec B_G2 EC305_MW_LAB	Sec B_G3 EC305_MW_LAB


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DATESHEET FOR
END TERM PRACTICAL EXAMINATION FOR PRS AND PRE COMPONENT
NOVEMBER 2025

CSE, IT, SE			
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM
14/11/2025	CS201_MP1_LAB_CSE1_G1	CS201_MP1_LAB_CSE1_G2	CS201_MP1_LAB_CSE1_G3
14/11/2025	SE201_MP2_LAB_SW1_G1	SE201_MP2_LAB_SW1_G2	SE201_MP2_LAB_SW1_G3
15/11/2025	CS201_MP1_LAB_CSE2_G1	CS201_MP1_LAB_CSE2_G2	CS201_MP1_LAB_CSE2_G3
15/11/2025	CS201_MP2_LAB_CSE6_G1	CS201_MP2_LAB_CSE6_G2	CS201_MP2_LAB_CSE6_G3
17/11/2025	CS201_MP1_LAB_CSE4_G1	CS201_MP1_LAB_CSE4_G2	CS201_MP1_LAB_CSE4_G3
17/11/2025	IT201_MP2_LAB_IT1_G1	IT201_MP2_LAB_IT1_G2	IT201_MP2_LAB_IT1_G3
18/11/2025	CS201_MP1_LAB_CSE3_G1	CS201_MP1_LAB_CSE3_G2	CS201_MP1_LAB_CSE3_G3
18/11/2025	IT201_MP2_LAB_IT2_G1	IT201_MP2_LAB_IT2_G2	IT201_MP2_LAB_IT2_G3
19/11/2025	EC105_MP1_LAB_CYBER_G1	EC105_MP1_LAB_CYBER_G2	EC105_MP1_LAB_CYBER_G3
19/11/2025	SE201_MP2_LAB_SW2_G1	SE201_MP2_LAB_SW2_G2	SE201_MP2_LAB_SW2_G3
19/11/2025	CS201_DE_LAB_CSE5_G2	CS201_DE_LAB_CSE5_G1	CS201_DE_LAB_CSE5_G3
19/11/2025	CS201_EDC2_LAB_CSE7_G1	CS201_EDC2_LAB_CSE7_G2	CS201_EDC2_LAB_CSE7_G3
19/11/2025	IT201_LIC_LAB_IT3_G2	IT201_LIC_LAB_IT3_G1	IT201_LIC_LAB_IT3_G3

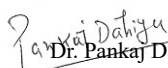
MP1 LAB (Microprocessor -1 Lab)	MP2 LAB (Microprocessor -II Lab)	DE LAB (Digital Electronics Lab)	EDC2 LAB (Electronics Devices Circuit- 2 Lab)
LIC LAB (Linear Integrated Circuit Lab)			


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DATESHEET FOR
END TERM PRACTICAL EXAMINATION FOR PRS AND PRE COMPONENT
NOVEMBER 2025

M. TECH VLSI, SPD, MOC	
Date	Session 4 04:00 PM-06:00 PM
14/11/2025	VLS501_VLSI_LAB
14/11/2025	SPD503_ADSP_LAB
14/11/2025	MOC501_MW_LAB
15/11/2025	VLS507_DE_LAB
15/11/2025	SPD507_VLSI_LAB
15/11/2025	MOC507_MW_LAB
17/11/2025	VLS541_MP1_LAB
17/11/2025	SPD541_EDC1_LAB
17/11/2025	MOC541_MW_LAB
18/11/2025	VLS601_MP1_LAB
18/11/2025	SPD601_EDC1_LAB
18/11/2025	MOC601_MW_LAB
19/11/2025	VLS503_VLSI_LAB
19/11/2025	SPD517_TELECOM_LAB
19/11/2025	MOC503_MW_LAB

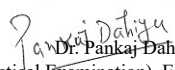

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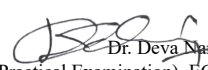

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DATESHEET FOR
END TERM PRACTICAL EXAMINATION FOR PRS AND PRE COMPONENT
NOVEMBER 2025

EC101: BASIC ELECTRONICS AND COMMUNICATION ENGINEERING				
Date	Session 1 9:00-11:00 AM	Session 2 11:00 AM-01:00 PM	Session 3 01:30 PM-03:30 PM	Session 4 04:00 PM-06:00 PM
14/11/2025	EC101_EDC1_LAB_SW3_G1	EC101_EDC1_LAB_SW3_G2	EC101_EDC1_LAB_SW3_G3	
14/11/2025	EC101_CF_LAB_SW1_G1	EC101_CF_LAB_SW1_G2	EC101_CF_LAB_SW1_G3	
14/11/2025	EC101_BOE_LAB_SW2_G1	EC101_BOE_LAB_SW2_G2	EC101_BOE_LAB_SW2_G3	
15/11/2025	EC101_EDC1_LAB_MC1_G1	EC101_EDC1_LAB_MC1_G2	EC101_EDC1_LAB_MC1_G3	
15/11/2025	EC101_CF_LAB_MC2_G1	EC101_CF_LAB_MC2_G2	EC101_CF_LAB_MC2_G3	
15/11/2025	EC101_BOE_LAB_MC3_G1	EC101_BOE_LAB_MC3_G2	EC101_BOE_LAB_MC3_G3	
17/11/2025	EC101_EDC1_LAB_BT_G1	EC101_EDC1_LAB_BT_G2	EC101_EDC1_LAB_BT_G3	
17/11/2025	EC101_CF_LAB_CSE2_G1	EC101_CF_LAB_CSE2_G2	EC101_CF_LAB_CSE2_G3	
17/11/2025	EC101_BOE_LAB_IT2_G1	EC101_BOE_LAB_IT2_G2	EC101_BOE_LAB_IT2_G3	
18/11/2025	EC101_EDC1_LAB_CSE1_G1	EC101_EDC1_LAB_CSE1_G2	EC101_EDC1_LAB_CSE1_G3	
18/11/2025	EC101_CF_LAB_IT1_G1	EC101_CF_LAB_IT1_G2	EC101_CF_LAB_IT1_G3	
18/11/2025		EC101_BOE_LAB_CSE3_G2	EC101_BOE_LAB_CSE3_G3	EC101_BOE_LAB_CSE3_G1
19/11/2025	EC101_EDC1_LAB_CSE4_G1	EC101_EDC1_LAB_CSE4_G2	EC101_EDC1_LAB_CSE4_G3	
19/11/2025	EC101_CF_LAB_CSE5_G1	EC101_CF_LAB_CSE5_G2	EC101_CF_LAB_CSE5_G3	
19/11/2025	EC101_BOE_LAB_CSE6_G1	EC101_BOE_LAB_CSE6_G2	EC101_BOE_LAB_CSE6_G3	

BOE LAB (Basic Electronics Lab)	EDC1 LAB (Electronics Devices Circuit-1 Lab)	CF LAB (Circuit Fabrication Lab/ Electronics Workshop)
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